

## **CURRICULUM VITAE**

DIPALI PRABHAKAR SARAG

(Dipali Ashok Ghule)

**M.E. (Electronics)**

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Flat No. 205, Ajay Heights, Opp. Kalpak lawns, Beed by Pass road, Aurangabad

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- **Objective:**

Seeking for techno – managerial assignment in an industrial organization to implement talent, knowledge and smart working skill for the growth.

- **Job Profile:**

**1) Organization:-** Maharashtra Institute of Technology, Chh.Sambhaji Nagar. M.S.

**Designation:-** Assistant Professor in Electronics & Computer Engineering. Department

**Working Period:-** December 2024 to till date.

**2) Organization:-** Deogiri Institute of Engineering and Management Studies, Chh.Sambhaji Nagar. M.S.

**Designation:-** Assistant Professor in Electronics & telecommunication Engineering Department

**Working Period:-** August 2023 to September 2024.

**3) Organization:-** CSMSS College of Polytechnic, Kanchanwadi, Chh.Sambhaji Nagar. M.S.

**Designation:-** Lecturer in Electronics & telecommunication Department

**Working Period:-** April 2022 to July 2023.

**4) Organization:-** Marathwada Institute of technology, Aurangabad.

**Designation:-** Visiting faculty for the subject of Development of skills and Emotional intelligence

**Working Period:-** March 2021 to august 2021, January 2022 to march 2022

**5) Organization:-** Hi – Tech Institute of Technology, P-119, Bajaj Nagar, Waluj M.I.D.C.Aurangabad.

**Designation:-** Lecturer in Electronics & telecommunication Department

**Working Period:-** August 2007 till August 2014

- **Job Responsibilities:**

Teaching assignment in the middle management level. Managing, handling, motivating batches of engineering students to build an engineers' society.

Enthusiastically supporting student activity as the in charge and coordinator of EESA, nourishing the professional development and growth of mentees under the mentor mentee head, etc.

Handled university activities such as Question Paper establishing, paper checking, and syllabus design.

Handled multiple committees and tried to follow their rules and regulations to ensure the institute could grow easily. Preparing documents to create a framework for purposeful, consistent, and catalytic improvement in institutions' overall performance.

- **Intellectual Property Rights**

- Title of the invention : Enhanced Safety Polyethylene-Coated Conductor for Eliminating Hooking-Based Electricity Theft and Fatal Accidents.

- Application No.202521025099 A INDIA

- Date of filing of Application :20/03/2025

- Publication Date : 28/03/2025

Status: Published

- **Certification**

- Government Certified Management , Entrepreneurship and Professional Sector Council (MEPSC) Trainer.
- Certified Retailers Association Skill Council of India (RASCI) Train er.
- Certified Telecom Sector Skill Council (TELECOM) Trainer.

- **Subject Taught:**

VLSI, Digital Electronics, Data structures and algorithm using java, Open Source technology, Mobile and wireless communication, computer network theory, Digital communication System, Network Security, C language, SC&ET, Microprocessor, Embedded System, Network Theory, EDC, Development of skills, Emotional intelligence,

- **Paper Presentation:**

International journal - “**VGA Monitor Interfacing with ARM7 Micrcontroller**” in ijird(International Journal of Innovative Research & Development), September 2014, Vol 3 Issue 9

State Level Paper Presentation – “**Wimax Technology**”

National level Paper Presentation – “**Network Security**”

- **Training Delivered:**

- ▲ 12 days volunteered online training on personality development at Smarthanam,Pune
- ▲ 1 week volunteered online training on workplace readiness and interview skillsfor Eon batch.
- ▲ 1 day online Seminar on “Interview Skills for Jobseeker” at CRS Training Center (CSR Project by TECH MAHINDRA FOUNDATION Project Partner-Gramodaya Sangh)

- **Training Taken:**

- ▲ One week Short term training on “Outcome based education” by NITTTR, Bhopal
- ▲ One week Short term training on “Connecting Science to society and Culture” by NITTTR, Bhopal
- ▲ 2 Days workshop on “Effective Teaching Techniques & Stress Management”
- ▲ 10 days training on “Integrating Security & software Engineering” by Indian Society for Technical Education, New Delhi at Mumbai.
- ▲ One week training on “Digital Signal Processing in Advance Communication” sponsored By AICTE
- ▲ 15 days in plant training at m/s Sai Electricals & Electronics Pvt.Ltd., Aurangabad
- ▲ One week training on “PLC & SCADA” by Krish Info tech Institute,Aurangabad

- **Projects Handled:**

VGA Interfacing with ARM7 Microcontroller , Auto Electric billing system using SMS, Portable Ozone generator, and Sequence Generator

- **Academic Qualification:**

- ▲ Masters of Engineering in Electronics (ME)  
Passed in First division with Distinction from Government College of Engineering, Aurangabad

- ▲ Bachelor of Engineering in Electronics(BE)  
Passed in First Division from Dr. BAM University, Aurangabad(M.S.) in June -2007
- ▲ Diploma in Electronics & Telecommunication(DTE)  
Passed in First Division from Government Polytechnic, Aurangabad in June -2004
- ▲ Secondary School Certificate(S.S.C.)  
Passed in First Division from Sharada Mandir High School, Aurangabad in 1999

● **Personal Particular:**

- ▲ Name : Dipali Prabhakar Sarag
- ▲ Date of Birth : December 15,1983
- ▲ Gender : Female
- ▲ Marital Status : Married(Dipali Ashok Ghule)
- ▲ Religion : Hindu
- ▲ Language Fluency : Marathi, Hindi, English
- ▲ Address : A wing, Flat no. 205,  
Ajay Heights Society,  
Near Kamal Nayan bajaj hospital,  
beed bypass road. Aurangabad – 431001 (M.S)
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- ▲ Contact No. : +919021557113

DATE :        /        /2025  
PLACE : Aurangabad

(Mrs. DIPALI P. SARAG)