

Curriculum Vitae

Name: Sarita Devanand Sanap

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Date of Birth:27/1/1980



Academic Credentials

Class/ Degree	Specialization	Institution	University	Year	%	Class
Ph.D.	Electronics		Dr.B.A.M.U			<i>Pursuing</i>
M.E	Electronics	J.N.E.C	Dr.B.A.M.U	2007	62.18	<i>First</i>
B.E	Electronics	J.N.E.C	Dr.B.A.M.U	2002	68.8	First with Distinction

M. Tech. Project:

Monitoring and Controlling of DG Set.

Key Research Areas:

VLSI Design

Network Security

Experience

Sr. No.	Organization	Post	From	To	No. of Years
1	Maharashtra Institute of Technology,Aurangabad	Assistant Professor	29/7/2011 to Till date		11
2	Marathwada Institute of Technology,Aurangabad	Assistant Professor	23/5/2006 to 23/9/2009		3.5

List of Courses Taught/Teaching at UG level

- VLSI Design
- Computer Networks and Security
- Embedded Systems
- Digital Communication
- Wireless and Mobile Communication
- Mechatronics
- Electronic Measurement Systems
- Microprocessor and Peripherals
- Radar and Satellite Communication

List of Courses Taught/Teaching at PG level:

Wireless and Mobile Communication

CMOS VLSI

IoT

Additional Assignments/Duties

- Working as Project Coordinator
- Working as Budget and FRA coordinator
- Working as Curriculum development coordinator
- Working as Lab In-charge of Communication System II lab.
- Working as module coordinator for VLSI Module.
- Worked as Seminar coordinator for UG and PG
- Worked as In Plant training coordinator
- Worked as Under Study for Dr.B.A.M.U Engineering examinations.
- Worked as Practical/Oral examination coordinator
- Worked as Lab In-Charge of Project Lab

Membership of Professional Bodies

Life Member of ISTE

Life Member of CRSI

Research Projects/Projects Guided

Computer/Software Proficiency

VHDL (Very High Speed Integrated Circuits Hardware Description Language)

Verilog (HDL for VLSI Design)

Cisco Packet Tracer (Network Simulator)

GNS3 (Network Simulator)

Wireshark (Network Simulator)

Kali Linux (Digital Forensics and Penetration Testing)

MATLAB

Modelsim (HDL simulator)

C and C++programming

Embedded C

Java

Cryptool (Network Security)

Seminar/Workshop/Industrial Training/STTP//FDP/CEP/Conference Attended

Participated and successfully completed the 5-day online FDP on the theme “Inculcating Universal Human Values in Technical Education” organized by All India Council for Technical Education(AICTE) from 28th March, 2022 to 1st April, 2022

Participated & completed successfully AICTE Training and Learning (ATAL) Academy Online Elementary FDP on "Research Issues in VLSI Design and Testing" from 2021-08-02 to 2021-08-06 at Government College of Engineering Srirangam.

Participated in orientation program on Advanced VLSI Design using Microwind by Dr.R D Gode institute of Technology and Research, Amravati during 6th May to 12th May 2021.

participated & completed successfully AICTE Training And Learning (ATAL) Academy Online FDP on "Block chain" from 2020-10- 19 to 2020-10-23 at Government Engineering College, Patan.

participated & completed successfully AICTE Training And Learning (ATAL) Academy Online FDP on "Cyber Security" from 2020- 9-7 to 2020-9-11 at BMS Institute of Technology and Management.

Participated in the One Week Online Faculty Development Program on “Network & IoT Simulation Platforms” under Technical Education Quality Improvement Program (TEQIP-III) Organized by Department of Information Technology, GEC Karad and Rajkiya Engineering College, Azamgarh During 27th – 31st August, 2020

Participated in Faculty Development program on Data Science organized by faculty development centre in Cyber Security and Data Sciences, Shivaji University, Kolhapur hosted by MIT during 24th June to 30th June 2019.

Attended one day workshop in Internet of Things organized by IGTR, Aurangabad on 23rd September 2019.

Participated in Faculty Development program on Cyber Security organized by faculty development centre in Cyber Security and Data Sciences, Shivaji University, Kolhapur hosted by Zeal college, Pune during 3rd June to 8th June 2019.

Participated in one day Faculty Development Program on Rubics Data Science organized by Rubics Lab at MIT on 26th June 2019.

Participated in two week ISTE STTP on CMOS, Mixed Signal and Radio Frequency Design conducted by IIT Kharagpur from 30th January to 4th February ,2017.

Participated in state level workshop on Exploring the world of Electromagnetics organized by ICEEM,Aurangabad on 16th and 17th January 2015.

Participated in one week STTP on System Design using FPGA organized by Government college of Engineering, Aurangabad during 24th June to 28th June 2013.

Attended one week STTP on Emerging Trends in Microwave and MicroStrip Devices organized by MIT from 7th May to 11th May 2012.

Participated in the workshop on LabView Programming organized by MIT and Trident Techlabs Pvt.Ltd.,Pune on 17th and 18th November 2011.

Attended one day workshop on CGPA system sponsored by Dr.B.A.M.U,Aurangabad at MIT on 5th November 2011.

Attended STTP on implementation of Digital Designs on FPGA Devices organized by MIT and BIT Mapper integration Technologies Pvt.Ltd.Pune on 3rd January to 5th January 2008.

Participated in one week workshop on Design and Implementation of Advanced Embedded Systems organized by MIT and ADM Technologies Pvt.Ltd., Mumbai at MIT from 30th June to 4th July 2008.

Participated in workshop on Embedded Systems and Robotics at Dr.B.A.M.U, Aurangabad on 24th and 25th March 2008.

Participated in three day workshop on Embedded System Design on MSP 430 organized by Gill instruments at MIT on 24th September to 26th September 2007.

Industrial Training at **HAL** on Manufacturing of aircraft components and units, Functional Test Lab Nasik from 26th June to 22nd July 2006.

Seminar/Workshop/Industrial Training/STTP//FDP/CEP/Conference organized

Worked as organizing team member of Techno-MIT2K19, a project competition on 4th and 5th October 2019.

Worked as organizing team member of TechnoMind2K18, a project competition held on 25th October 2018.

Worked as Finance Chair in international conference on Digital Signal and Image Processing on 7th and 8th March 2017.

Worked as session chair in international conference on Digital Signal and Image Processing on 7th and 8th March 2017.

Worked as Finance Chair in Applied Electromagnetics Conference from December 19 to December 22, 2017

Worked as coordinator in national level IETE sponsored workshop on Research Methodology on 13th and 14th March 2015

Organized two days workshop on Design and development of Raspberry Pi based Systems on 5th and 6th October 2013.

Organized a two day workshop on Computer Networks on 4th and 5th September 2014.

Worked as coordinator in three days workshop on Electronics and Circuit Foundation.

Worked as coordinator in national level STTP on challenges in CMOS VLSI on 8th November to 10th November 2008

Projects, Research Grants and Consultancy

Intellectual Property Rights

List of Research Publications

Papers in National /International Conference Proceedings

- Presented paper entitled “Performance Analysis of Encryption Technique based on Avalanche effect and Strict Avalanche effect criterion” the 3rd IEEE international conference on signal processing and communication held at Karunya institute of Technology and sciences, Coimbatore during 13th -14th May 2021.
- Presented paper entitled “Analysis of Encryption Techniques for Secure Communication” in 3rd IEEE International Conference on Emerging Smart Computing and Informatics during 5th - 7th March 2021.

- Presented paper “A Review on Vehicular Pollution Monitoring using IOT” in international conference on emerging trends in Engineering and Management, March 2017
- Presented paper “A Review: Border Gateway Protocol” in International Conference on Digital Signal and image processing, March, 2017
- Presented paper on “Frame Relay Congestion control and efficiency improvement” in international conference on current trends and challenges in Management, Engineering, Computer application and Technology, March 2012
- Published paper entitled “Vehicular Pollution Monitoring Using IOT” in International Journal for Research in Applied Science & Engineering Technology, Volume 9, Issue XII, December 2021
- Published paper entitled “Influencing BGP Path Selection Using Weight and Local Preference” in international journal of innovative research and in Science, Engineering and Technology, Volume 10, Issue 12, December 2021
- Published paper entitled “A review on vehicular pollution monitoring using IoT in international journal of Electrical and Electronics Engineering, Volume 9, Issue 1, January 2017

NPTEL Certification

Start Date	End Date	Duration (in Weeks)	Course Name	Marks (Out of 100)	Performance
July 2017	Sep 2017	8	Introduction to Research	75	Elite
July 2017	Aug 2017	4	OB Pedagogic principles for effective teaching	59	
29 July 2019	20 Sep 2019	8	TALE 2	78	Elite Silver

Date: 11/5/22

Place: Aurangabad