Curriculum Vitae

Name: MANASI RAJESH VARGANTWAR

Address: 23, Saffron Tranquil Landscape, Near Wakhar Mahamandal, Beed Bypass, Aurangabad

E-mail: manasi.vargantwar@mit.asia/@gmail.com

Mobile:+91 94227 06492/+91

Dateof Birth:01.07.1973

AcademicCredentials

Class/ Degree	Specialization	Institution	University	Year	%/CGPA	Class
Ph. D.	Electronics	Government EngineringCollege, Aurangabad	Dr. B. A. M. U. Aurangabad			Pursuing
M.E.	Electronics	Government EngineringCollege, Aurangabad	*Dr. B.A.M.U./ GECA*	*2008*	*78.75*	*First Division with Distinction*
PGDIM		NIT, Nagpur	*Nagpur University/ VRCE, Nagpur*	*1996*	*70.63*	*I Division*
B.E.	Electronics	Walchand Institute of Technology, Aurangabad	*Shivaji Universtiy/ WIT, Solapur*	*1994*	*73.47*	*First Class with Distinction*

M.E.Project:

Performance Analysis of different Thresholding Techniques   for Image Compression

KeyResearchAreas:Signal Processing, VLSI

Experience

Sr. No.	Organizati on	Post	From	To	Years	Months
1	Marathwada Institute of Technology , Aurangabad	Associate Professor, Department of ETC	24. 06.2009	Till Date	13 Yrs.	5
2	Maharashtra Institute of Technology , Aurangabad	Assistant Professor, Department of ETC	01.07.2008	18.06.2009	1	0
3	M. I. T. Polytechnic, Aurangabad	Lecturer, Department of ETC	10.07. 99	30.06.08	9	0
4	Marathwada Institute of Technology , Aurangabad	Lecturer, Department of ETC	06.08.98	17.06.99	0	10

Listof CoursesTaught/TeachingatUGlevel

- O   Digital Signal Processing

- O   Signals and Systems

- O   Digital Image Processing

- O   Radar and Satellite Communication

- O   VLSI Design

- O   Electronic Circuit Technology

- O   Data Structures and Algorithms

- Python Programming

Listof CoursesTaught/TeachingatPGlevel

- Advanced Digital Signal Processing

- Advanced Optimization Techniques
- Robotics and Machine Vision
- Advanced Digital System Design
- Design and Analysis of Algorithms

AdditionalAssignments/Duties

1. Working as Class Test, Term Work marks –Verifi9cation and Submission Co-ordinator and University Practical Examinations Co-ordinator
2. Working as Disciplinary Committee Member for the Institute
3. Worked as DSY admission committee member
4. Worked as Incharge, Exam Control Cell for Dr. BAMU, Aurangabad and Dr. BATU, Lonere examinations.
5. Worked as P. G. Co-ordinator of Department of Electronics and Telecommunication Engineering
6. Signed 2, MoUs with Texas Instruments, India as MIT member (Analog Lab and Embedded Lab)-Received Kits in both MoUs
7. Arranged Texas Instruments "Innovation Challenge-India Analog Design Contest-2014 and 2015 -Winners received MSP Chronos Watches
8. Worked as NBA, AICTE, University Affiliation, NIRF, Shikshan Shulka Samiti etc., Institute Co-ordinator
9. Worked as PEO coordinator (for NBA) at Department of ECE
10. Arranged two days workshop on " Hands-on Workshop on Arduino" for the students
11. Arranged two days workshop on "Matlab and Its Applications" for the students of MIT and other Engineering Colleges
12. Was a resource person for the workshop on "MATLAB" conducted for faculty
13. Worked as Lab in Charge of Computer Laboratory , DSP/VLSI Laboratory
14. Worked as a lab in charge of Endress+ Hauser laboratory
15. Worked as an Organizing secretary of National Conference on "Signal Processing: Exceeding Technological Horizon" Organized at Maharashtra Institute of Technology, Aurangabad on 5th-6th March 2009.
16. Worked as IPT Coordinator for the ETC Department
17. Worked as an Chief Suprintendent (Dr. BAMU, Aurangabad exams) and as ATA in MMS / MBA Entrance Examination.
18. Worked as admission committee member, Technomillenium, Annual Social gathering committee and head of various committees.
19. Arranged Industrial Visits, Seminars and Guest Lectures.

MembershipofProfessionalBodies

- Life Member of ISTE (LM-41077)
- Life Member of IETE (M-177604)

ResearchProjects/ProjectsGuided

PG Projects Guided: 19
UG Projects Guided: Several

Computer/SoftwareProficiency

- MATLAB
- VHDL
- Python Programming

Seminar/Workshop/IndustrialTraining/STTP//FDP/CEP/ConferenceAttended

Sr. No.	STTP / Training Programs	Name Of the Program	Place	Duration
1.	Workshop	MATLAB 6.5 and Applications	GEC, Aurangabad	21,22 Feb 2004
2.	STTP	Microcontroller Systems Programming and Applications	GEC, Aurangabad	17,18 March 2005
3.	STTP	Recent Trends in Signal and Image Processing	SGGS Nanded	16,17,18 January 2006
4.	Training Program	Wavelets and Advanced Wavelets	GEC, Aurangabad	31 Jan., 1,2 February 2006
5.	Workshop	Advanced Webpage Design and Multimedia	MIT, Aurangabad	16-26 July 2006
6.	STTP	Microcontrollers and Embedded Systems	G. P., Aurangabad	22 Oct.-03 November 2007
7.	STTP	Implementation of Digital Designs on FPGA Devices	MIT, Aurangabad	3,4,5 January 2008
8.	Workshop	Design and Implementation of Advanced Embedded Systems	Maharashtra Institute of Technology, A'bad	30 June-4 July 2008

9.	STTP	Challenges in CMOS VLSI Design	MIT, Aurangabad	8,9,10 November 2008
10.	STTP	Advances in Antenna and RFIC/MMIC Design	K.J. Sommaiya COE, Mumbai	24-28 June 2013
11.	Workshop	Outcome Based Education	Maharashtra Institute of Technology, Aurangabad	27th September 2014
12.	International Workshop	Reconfigurable and Multiple Beam Antenna Technology	Ahmedabad	13 December 2014
13.	State Workshop	Exploring the World of Electromagnetics	ICEEM, Aurangabad	16,17 January 2015
14.	FDP	Soft Computing-ANN and Fuzzy Applications in Engineering	Deogiri Institute of Engineering and Management Studies, Aurangabad	30 Nov-5 December 2015
15.	Workshop	National Institutional Ranking Framework	Deogiri Institute of Engineering and Management Studies, Aurangabad	10 January 2016
16.	FDP	Internet of Things (IoT)	MIT, Aurangabad	11-16 July 2016
17.	STTP	CMOS, Mixed Signal and Radio Frequency VLSI Design	IIT, Kharagpur at MIT, Aurangabad Remote Center	30 January-04 February 2017
18.	Workshop	Intellectual Property Rights and Opportunities for Funded Reasearch	Maharashtra Institute of Technology, Aurangabad	5th and 6th January 2018
19.	FDP	Recent Trends in Electromagnetics, RF and Radar Signal Processing	GEC, Aurangabad	11th to 15th June 2018
20	STTP	Advanced Engineering Optimization through Intelligent Techniques	SV NIT, Surat	13th to 17th MAY 2019
21	FDP	Instrial IoTs, Industry 4.0 and Disruptive Technologies	Online	5th to 10th May 2020
22	National	E Contents and ICT Tools for	Online	15th to 21 st July

	FDP	Innovative and Effective Teaching		2020
23	Workshop	Inculcating Universal Human Values in Technical Education	Online	2nd-6th November 2020
24	Workshop	OutCome Based Education	Online	20, 21 August 2021
25	Capacity Building Programme / FDP	Cybersecurity and Forensics	Online	19-24 June 2023

Seminar/Workshop/IndustrialTraining/STTP//FDP/CEP/Conferenceorganized

- Arranged two days workshop on "Matlab and Its Applications" for the students of MIT and other Engineering Colleges

- Worked as an Organizing secretary of National Conference on "Signal Processing: Exceeding Technological Horizon" Organized at Maharashtra Institute of Technology, Aurangabad on 5th-6th March 2009.

- Organized and was a resource person for the workshop conducted for faculty members on "MATLAB"

- Arranged two days workshop on " Hands-on Workshop on Arduino" for the students

- Arranged Web Based Training programs for the students at Endress+Hauser, Aurangabad

ListofResearchPublications

Sr. No.	Title of the Paper	Journal Name	Volume & Issue	Published Year	Publisher
1.	Fingerprint Identification using Radon Transform	International Journal of Graphics and Image Processing	Vol. 1 Issue 2	November 2011	International Forum of Researchers, Students and Academicians
2.	Real Time Embedded Implementation of Face Feature Detection System on Beagle Board-XM	International Journal of Management, IT and Engineering	vol. 4 Issue 8,	August 2014	IJMIE

3.	IRIS Recognition using Neural Network	International Journal of Innovative Research in Science Engineering and Technology	Special Issue	February 2014	IJIRSET
4.	HDLC Protocol Implementation using VHDL	International Journal of Innovative Research in Science Engineering and Technology	Special Issue	February 2014	IJIRSET
5.	Pentagonal Planar Monopole Antenna for Ultra Wide Bandwidth	International Journal of Multidisciplinary Educational Research	Vol. 3 Issue 3(9)	March 2014	IJMER
6.	Video Based tracking with Mean-Shift and Kalman Filter	International Journal of Engineering Research and Technology	Vol. 3 Issue 5	May 2014	IFERT
7.	Designing of DA Based Adding and Shifting, LUT Based FIR Filters on FPGA	International Journal of VLSI System Design and Communication Systems	Vol. 2 Issue 5	August 2014	SEMAR Groups Technological Society
8.	Online Examination System using Embedded Device	International Organization of Scientific Research	Vol. 6 Issue 11	November 2016	International Organization of Scientific Research
9.	Raspberry Pi Based Online Examination System	International Journal of Innovative Research in Science Engineering and Technology	Vol. 5 Issue 9	September 2016	International Journal of Innovative Research in Science Engineering and Technology
10	IoT enabled smart garbage management and segregation	International Journal of Emerging Technologies and Innovative Research	Vol. 6, Issue 6	July 2019	International Journal of Emerging Technologies and Innovative Research
11	IoT Based Road side unit for Intelligent Transportation system	International Journal of Modernization in Engineering Technology and Science	Vol. 3, Issue 3	March 2021	International Journal of Modernization in Engineering Technology and Science
12	Performing Text Modeling and Sentiments Analysis using LDA and Deep Learning	Journal of Emerging Technologies and Innovative Research	Vol. 9, Issue 12	March 2022	Journal of Emerging Technologies and Innovative Research
13	Smart	International Journal of	Vol 10,	March 2022	International

	Hydrophonic monitoring and controlling system using IoT	Research in Engineering and Science	Issue 3		Journal of Research in Engineering and Science

PapersinInternationalConferencesProceedings

Sr. No.	Title of the Paper	Conference Name	Conference Level (National/Inter national)	Conference Venue & Organizer	Dates
1.	Classification of EEG using PCA, ICA and Neural Network	International Conference in Computational Intelligence	International	International Journal Computer Applications	2011
2.	Classification of EEG using PCA, ICA and Support Vector Machine	International Conference on Computers and Communication	International	SISTec-ICCC-2012	2012
3.	Video Based Tracking and Optimization using Mean Shift , Kalman Filter and Swarm Intelligence	Computing Communication Control and Automation (ICCUBEA), 2015	International	IEEE	2015

Awards,AchievementsandRecognition

- Obtained Certificate of appreciation for being the organizer on behalf of MIT for Analog Maker competition 2014 conducted by Texas Instruments India University Program in association with Edgate Technologies and MIT

- Awarded with "Women's achievement award" 2017" given by Arya Vaishya Lions Aurangabad

Date:01.09.2022

Place:Aurangabad